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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD AND SYSTEM FOR PERFORMING STATIC TIMING ANALYSIS ON DIGITAL ELECTRONIC CIRCUITS	
Application Number :		
Date :		
First Named Applicant:	Steven E. Charlebois	
Confirmation Number:		
Attorney Docket Number:	BUR920040056US1	
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Submitted By:	Elec. Sign.	Sign. Capacity
Richard A. Henkler Esq. Registered Number: 39220	/Richard A. HENKLER, Reg. #39220	Attorney

Documents being submitted:	Files
us-ids	BUR920040056US1-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	BUR920040056US1-usassn.xml us-assignment.xsl us-assignment.dtd
us-request	BUR920040056US1Assign.tif BUR920040056US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920040056US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	BUR920040056US1Dec1.tif
us-declaration	BUR920040056US1Dec2.tif
application-body	BUR920040056US1spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd BUR920040056US1FIGSsheet1of5.tif BUR920040056US1FIGSsheet2of5.tif BUR920040056US1FIGSsheet3of5.tif BUR920040056US1FIGSsheet4of5.tif BUR920040056US1FIGSsheet5of5.tif
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